

AMENDMENTS TO THE CLAIMS

Please **AMEND** claims 11-15 as shown below.

The following is a complete list of all claims in this application.

1-10. (Cancelled)

11. (Currently Amended) A ~~thin film transistor array substrate for a liquid crystal display (LCD)~~, comprising:

~~an insulating a first substrate;~~

~~a gate line formed on the first substrate;~~

~~a storage capacitor line proceeding parallel to the gate line;~~

~~a storage capacitor electrode connected to the storage capacitor line;~~

~~a gate insulating layer formed on the gate line, the storage capacitor line and the storage capacitor electrode;~~

~~a data line formed on the gate insulating layer first substrate and intersecting the gate line;~~

~~a pixel region defined by the gate line and the data line;~~

~~a storage capacitor line formed on the first substrate and comprising a storage capacitance electrode;~~

~~a protective layer formed on the data line; and~~

a pixel electrode formed on the protective layer with opening patterns first substrate and entirely covering a portion of the storage capacitance electrode;
a second substrate facing the first substrate; and
a common electrode formed on the second substrate and having a first opening pattern overlapping the portion of the storage electrode entirely covered by the pixel electrode,
~~wherein the storage capacitor electrode is extended along an edge of the pixel region, and the pixel electrode entirely covers a portion of the storage capacitor electrode.~~

12. (Currently Amended) The ~~thin film transistor array substrate LCD~~ of claim 11, wherein the storage capacitor electrode is formed at a left side and a right side of the pixel region.

13. (Currently Amended) The ~~thin film transistor array substrate LCD~~ of claim 11, wherein the storage capacitor line comprises two lines in the pixel region.

14. (Currently Amended) The ~~thin film transistor array substrate LCD~~ of claim 11, wherein the pixel electrode has a second each opening pattern comprises comprising:
~~a horizontal opening first portion formed at a portion of the pixel electrode dividing the pixel electrode into an upper region and a lower region; and~~
~~inclined opening portions second portions formed at the upper region and the lower region and of the pixel electrode while proceeding perpendicular to each other.~~

15. (Currently Amended) A thin film transistor array substrate liquid crystal display, comprising:

an insulating a first substrate;

a second substrate facing the first substrate;

a pixel region formed on the insulating substrate;

a storage capacitor line formed on the first substrate and having a main portion and a branch portion extended from the main portion and formed on the insulating substrate, the branch portion extended along an edge of the pixel region; and

a pixel electrode insulated from the storage capacitor line and entirely covering a portion of the branch portion having a first opening pattern, wherein the pixel electrode entirely covers a portion of the branch portion; and

a common electrode formed on the second substrate and having an opening pattern overlapping the portion of the branch portion entirely covered by the pixel electrode.